## **Electronic Acknowledgement Receipt** EFS ID: 1007969 **Application Number:** 10595157 **Confirmation Number:** 3496 Title of Invention: Method of manufacturing multi-layer circuit board First Named Inventor: Toshiaki Takenaka **Customer Number:** 52349 Filer: Charles R. Watts Filer Authorized By: **Attorney Docket Number:** 2006-0223A **Receipt Date:** 09-MAR-2006 Filing Date: Time Stamp: 10:32:00 **Application Type:** U.S. National Stage under 35 USC 371 Payment information:

	Submitted with Payment	yes
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## **File Listing:**

Document Number	ocument Description	File Name	File Size(Bytes)	Multi Part	Pages
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1	Application Data Sheet	ADSform.pdf	4931996	no	5
Warnings:					
2	Documents submitted with 371 Applications	pctapplication.pdf	848546	no	27
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3	Oath or Declaration filed	Declaration.pdf	108165	no	3
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	Abstract		26	26	
	Drawings	<b>;</b>	27	33	2
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5		references.pdf	2103127	yes	42
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Information Disclosure Statement (IDS) Filed	Form1449.pdf	76429	no	1
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Information Disclosure Statement (IDS) Filed	IDSFORM.pdf	84434	no	3
Documents submitted with 371 Applications	PCTFORM.pdf	120672	no	2
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## New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

## National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.